



POLITÉCNICA

INTERNATIONAL  
CAMPUS OF  
EXCELLENCE

COORDINATION PROCESS OF  
LEARNING ACTIVITIES  
PR/CL/001



E.T.S. de Ingeniería y Sistemas  
de Telecomunicación

# ANX-PR/CL/001-01

## LEARNING GUIDE

### SUBJECT

**593000501 - Embedded Platforms And Communications For Iot**

### DEGREE PROGRAMME

59AH - Master Universitario En Internet Of Things (iot)

### ACADEMIC YEAR & SEMESTER

2019/20 - Semester 1

## Index

---

### Learning guide

1. Description.....	1
2. Faculty.....	1
3. Prior knowledge recommended to take the subject.....	3
4. Skills and learning outcomes .....	3
5. Brief description of the subject and syllabus.....	4
6. Schedule.....	6
7. Activities and assessment criteria.....	8
8. Teaching resources.....	11

## 1. Description

---

### 1.1. Subject details

<b>Name of the subject</b>	593000501 - Embedded Platforms And Communications For Iot
<b>No of credits</b>	4.5 ECTS
<b>Type</b>	Compulsory
<b>Academic year of the programme</b>	First year
<b>Semester of tuition</b>	Semester 1
<b>Tuition period</b>	September-January
<b>Tuition languages</b>	English
<b>Degree programme</b>	59AH - Master Universitario En Internet Of Things (IOT)
<b>Centre</b>	59 - Escuela Técnica Superior de Ingeniería y Sistemas de Telecomunicación
<b>Academic year</b>	2019-20

## 2. Faculty

---

### 2.1. Faculty members with subject teaching role

<b>Name and surname</b>	<b>Office/Room</b>	<b>Email</b>	<b>Tutoring hours *</b>
Eduardo Barrera Lopez De Turiso	A4203	eduardo.barrera@upm.es	M - 16:30 - 17:30 Schedules are subject to possible changes. Please, confirm the latest version with the lecturer

Ruben Diego Martinez	A4411	ruben.de.diego@upm.es	M - 16:30 - 17:30 Schedules are subject to possible changes. Please, confirm the latest version with the lecturer
Vicente Hernandez Diaz	A4412	vicente.hernandez@upm.es	M - 16:30 - 17:30 Schedules are subject to possible changes. Please, confirm the latest version with the lecturer
Miguel Chavarrias Lapastora	A4406	miguel.chavarrias@upm.es	M - 16:30 - 17:30 Schedules are subject to possible changes. Please, confirm the latest version with the lecturer
Eduardo Juarez Martinez (Subject coordinator)	A4204	eduardo.juarez@upm.es	M - 16:30 - 17:30 Schedules are subject to possible changes. Please, confirm the latest version with the lecturer
Sergio Esquembri Martinez	A4206	s.esquembri@upm.es	M - 16:30 - 17:30 Schedules are subject to possible changes. Please, confirm the latest version with the lecturer

\* The tutoring schedule is indicative and subject to possible changes. Please check tutoring times with the faculty member in charge.

### 3. Prior knowledge recommended to take the subject

---

#### 3.1. Recommended (passed) subjects

The subject - recommended (passed), are not defined.

#### 3.2. Other recommended learning outcomes

- Application of processor interrupts
- Programming and debugging using the C language (with emphasis on structures, pointers and memory management)
- Application of the Von Neumann's computer architecture
- Application of processor peripherals

### 4. Skills and learning outcomes \*

---

#### 4.1. Skills to be learned

CB06 - Poseer y comprender conocimientos que aporten una base u oportunidad de ser originales en el desarrollo y/o aplicación de ideas, a menudo en un contexto de investigación

CB10 - Que los estudiantes posean las habilidades de aprendizaje que les permitan continuar estudiando de un modo que habrá de ser en gran medida autodirigido o autónomo.

CE.01 - Evaluar las arquitecturas HW/SW existentes y diseñar plataformas embebidas para desarrollar soluciones IoT integrando diversas capas SW que manejen sensores, actuadores y dispositivos de comunicación

CE.02 - Diseñar y desarrollar redes de sensores integrando nodos heterogéneos con diferentes sistemas de comunicación inalámbricas para desarrollar aplicaciones IoT

CG01 - Los alumnos demostrarán tener una visión del estado actual, las necesidades y los problemas que se plantean en el mundo de la IoT, así como de las arquitecturas y estándares más utilizados

CG02 - Los alumnos serán capaces de aplicar métodos y tecnologías avanzadas que les permitan abordar necesidades y problemas en aplicaciones IoT

CT.01 - Capacidad de uso de la lengua inglesa para el trabajo en contextos internacionales

## 4.2. Learning outcomes

RA24 - To integrate a wireless technology and the communication protocols for a hardware platform in an IoT specific application

RA22 - To combine the development tools for the integration of all software elements required to use a hardware platform in an IoT solution

RA21 - To establish the building or selection criteria of embedded hardware platforms for the integration of a specific IoT application

RA23 - To use the interfaces for the connection of sensors, actuators and communication modules in a hardware/software platform supporting IoT applications

\* The Learning Guides should reflect the Skills and Learning Outcomes in the same way as indicated in the Degree Verification Memory. For this reason, they have not been translated into English and appear in Spanish.

## 5. Brief description of the subject and syllabus

---

### 5.1. Brief description of the subject

#### Brief Description of the Course

The main goal of this course is to introduce the hardware and software architectures frequently used in hardware embedded platforms for IoT applications. More specifically, the description of the essential hardware resources, architecture details and sensor and actuator interfaces are covered. In addition, the integration, development and debugging of software IoT applications are studied in detail.

## 5.2. Syllabus

1. IoT Introduction
  - 1.1. General Introduction
  - 1.2. Technology Solutions
  - 1.3. Use-case Description
  - 1.4. Communication Fundamentals
  - 1.5. Platform Architecture
  - 1.6. Demo
2. Mbed/Keil: the Software Perspective
  - 2.1. Mbed & Keil Vision
  - 2.2. I/O with Mbed
  - 2.3. Mbed Operating System
  - 2.4. Hands-on Lab
  - 2.5. Git Basics
3. Hw Architectures and I/O Interfaces: the Hardware Perspective
  - 3.1. STM32 Hw Description
  - 3.2. Serial Interfaces: I2C
  - 3.3. Serial Interfaces: SPI
  - 3.4. Serial Interfaces: RS-232
4. Project

## 6. Schedule

### 6.1. Subject schedule\*

Week	Face-to-face classroom activities	Face-to-face laboratory activities	Other face-to-face activities	Assessment activities
1	<p><b>Lesson 1: General Introduction</b> Duration: 02:00</p> <p><b>Lesson 1: Technology solutions</b> Duration: 02:00</p> <p><b>Lesson 1: Use-case description</b> Duration: 01:00</p>			
2	<p><b>Lesson 1: Communication fundamentals</b> Duration: 03:00</p> <p><b>Lesson 1: Platform Architecture</b> Duration: 01:00</p>	<p><b>Lesson 1: Demo</b> Duration: 01:00</p>		
3		<p><b>Lesson 2: Mbed &amp; Keil Vision</b> Duration: 02:00</p> <p><b>Lesson 2: I/O with Mbed</b> Duration: 03:00</p>		
4		<p><b>Lesson 2: Mbed Operating System</b> Duration: 02:00</p> <p><b>Lesson 2: Hands-on Lab</b> Duration: 02:00</p> <p><b>Lesson 2: Git basics</b> Duration: 00:45</p>		<p><b>Lesson 2: Hands-on exercises to assess proficiency</b></p> <p>Continuous assessment Duration: 00:15</p>
5	<p><b>Lesson 3: STM32 Hw Description</b> Duration: 05:00</p>			
6		<p><b>Lesson 3: Serial Interfaces: I2C</b> Duration: 02:00</p> <p><b>Lesson 3: Serial Interfaces: SPI</b> Duration: 01:00</p> <p><b>Lesson 3: Serial Interfaces: RS-232</b> Duration: 01:45</p>		<p><b>Lesson 3: Hands-on exercises to assess proficiency</b></p> <p>Continuous assessment Duration: 00:15</p> <p><b>Lesson 4: Preliminary Report Assessment on Course Project</b></p> <p>Continuous assessment</p>



				Duration: 00:30
7		<b>Lesson 4: Course Project</b> Duration: 05:00		
8		<b>Lesson 4: Course Project</b> Duration: 03:00		<b>Lesson 4: Project Assessment</b>  Continuous assessment Duration: 02:00  <b>Lesson 4: Report Assessment on Course Project</b>  Continuous assessment Duration: 01:00
9				
10				
11				
12				
13				
14				
15				
16				<b>Course Project Exam</b>  Final examination Duration: 03:00  <b>Proposed Exercises Exam</b>  Final examination Duration: 02:00
17				

The independent study hours are training activities during which students should spend time on individual study or individual assignments.

Depending on the programme study plan, total values will be calculated according to the ECTS credit unit as 26/27 hours of student face-to-face contact and independent study time.

\* The subject schedule is based on a previous theoretical planning of the subject plan and might go through experience some unexpected changes along throughout the academic year.

## 7. Activities and assessment criteria

### 7.1. Assessment activities

#### 7.1.1. Continuous assessment

Week	Description	Modality	Type	Duration	Weight	Minimum grade	Evaluated skills
4	Lesson 2: Hands-on exercises to assess proficiency		Face-to-face	00:15	15%	5 / 10	CG02 CE.01 CT.01 CG01 CB06 CB10
6	Lesson 3: Hands-on exercises to assess proficiency		Face-to-face	00:15	15%	5 / 10	CG02 CE.01 CT.01 CG01 CB06 CB10
6	Lesson 4: Preliminary Report Assessment on Course Project		No Presential	00:30	10%	5 / 10	CG02 CE.01 CT.01 CG01 CE.02 CB06 CB10
8	Lesson 4: Project Assessment		Face-to-face	02:00	40%	5 / 10	CG02 CE.01 CT.01 CG01 CE.02 CB06 CB10
8	Lesson 4: Report Assessment on Course Project		No Presential	01:00	20%	5 / 10	CG02 CE.01 CT.01 CG01 CE.02 CB06 CB10

#### 7.1.2. Final examination

Week	Description	Modality	Type	Duration	Weight	Minimum grade	Evaluated skills
16	Course Project Exam		Face-to-face	03:00	70%	5 / 10	CG02 CE.01 CT.01 CG01 CE.02 CB06 CB10
16	Proposed Exercises Exam		Face-to-face	02:00	30%	5 / 10	CT.01 CG01 CB06 CG02 CE.01 CB10

### 7.1.3. Referred (re-sit) examination

Description	Modality	Type	Duration	Weight	Minimum grade	Evaluated skills
Course Project Exam		Face-to-face	03:00	70%	5 / 10	CT.01 CG01 CE.02 CB06 CB10 CG02 CE.01
Proposed Exercises Exam		Face-to-face	02:00	30%	5 / 10	CG02 CE.01 CT.01 CG01 CB06 CB10

## 7.2. Assessment criteria

### Continuous Assessment

The continuous assessment will be carried out based in the following components:

- Exercises proposed for each course lesson
- A final project consisting of use-case building around the selected platform

The course grade will be composed of the following elements:

- Assessment of the proposed exercises: 30% (3.0 points)
- Assessment of the final project: 70% (7.0 points)

In each of the elements, a grade greater than or equal to 5.0 points is required

### "Solo Prueba Final" Assessment

The "Solo Prueba Final" assessment will consist of the following components:

- Course Project Exam: it is a written and practice exam
- Oral exam about the exercises proposed along the course

The grade will be composed of the following elements:

- Course Project: 70% (7 points)
- Oral exam: 30% (3 points)

In each of the elements, a grade greater than or equal to 5.0 points is required

### "Examen Extraordinario" Assessment

The "Examen Extraordinario" assesment will consist of the same components and weights as those of the "Solo Prueba Final" assesment

## 8. Teaching resources

---

### 8.1. Teaching resources for the subject

Name	Type	Notes
Fast and Effective Embedded Systems Design (Second Edition) Rob Toulson, Tim Wilmshurst	Bibliography	
The Designer's Guide to the Cortex-M Processor Family (2016 Second Edition) Trevor Martin	Bibliography	
Designing Embedded Systems and the Internet of Things (IoT) with the ARM mbed, Perry Xiao	Bibliography	
IoT Platform	Equipment	
UPM Moodle site	Web resource	